

Notice of Allowability

Application No.

09/889,410

Examiner

Kevin Schubert

Applicant(s)

KITAHARA, J

Art Unit

2137

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 2/7/06.
2. ☒ The allowed claim(s) is/are 2-8,14-16,18.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


EMMANUEL L. MOISE
SUPERVISORY PATENT EXAMINER

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Allowable Subject Matter

All pending claims have been deemed allowable over the prior art. The two independent claims (claim 16 and claim 18 in the claims submitted 2/7/06) recite an information processing apparatus which Examiner did not find in the prior art. In particular, Examiner submits that while the three pertinent
5 references cited in the previous action (i.e. Heer, Imai, and Stokes) account for features of the information processing apparatus, the information processing apparatus described contains functionality not found in the cited references or the prior art as a whole. For example, Examiner finds no disclosure of a single semiconductor chip processing device which contains cryptographic functionality as claimed along with data protection functionality to delete first key information in volatile memory via power supply in response
10 to an abnormality AND which *"voids a control signal to said volatile memory until an internal logical block in said processing device is finished initializing"* (see claim 16 and substantially similar language in claim 18).

With regard to the italicized portion, Applicant has described that a gate fixedly nullifies all the signals to volatile memory until initialization of the internal logic block is completed with the main power
15 supply turned on. Further, Applicant teaches that even under condition that only the key custody area is supplied with power while the other circuits are not supplied with power, it is possible to eliminate an excess leakage current (Specification: page 23 bottom four lines to page 24, line 3). Examiner finds no disclosure of the italicized limitation in conjunction with the single semiconductor chip with cryptographic and data protection functionality as claimed. For at least this reason, Examiner believes the instant
20 claimed invention patentable over the prior art.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Schubert whose telephone number is (571) 272-4239. The examiner can normally
25 be reached on M-F 7:30-6:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Emmanuel Moise can be reached on (571) 272-3865. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

- Information regarding the status of an application may be obtained from the Patent Application
- 5 Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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